

The top of the page features a blue square logo with the word "FRAMOS" in white. Below it, a row of green printed circuit boards (PCBs) is shown, each with two yellow circular mounting holes and the word "FRAMOS" printed in white. The boards are slightly offset, creating a sense of depth. The background of the entire page is a dark, textured grey.

**FRAMOS**

# EMBEDDED IMAGE SENSOR ECOSYSTEM

**Fast and Easy Vision Development from Concept to Production**

Embedded Vision applications have specific vision requirements. Image sensors (IMS) are not only part of stand-alone camera systems anymore. Today, IMS are very tightly integrated with processors into heterogeneous „Embedded Systems“. The Framos Ecosystem of Sensor Modules and Adapters provides vision engineers and developers with ready-to-use compositions of hardware, software and reference designs to accelerate time-to-market and optimize resources.

## THE FRAMOS ECOSYSTEM OF SENSOR MODULES AND ADAPTERS

Every Embedded Vision system has specific requirements that in most cases only a custom solution can solve. The integrated imaging application is realized by connecting image sensors directly to SoCs (System-on-Chip) providing outstanding efficiency, by integrating powerful micro-processors with dedicated ISP, GPU, and AI engines into one device. With the FRAMOS Embedded Vision Ecosystem of interchangeable components, vision engineers and developers can quickly and easily connect those components and develop their products from concept to market.

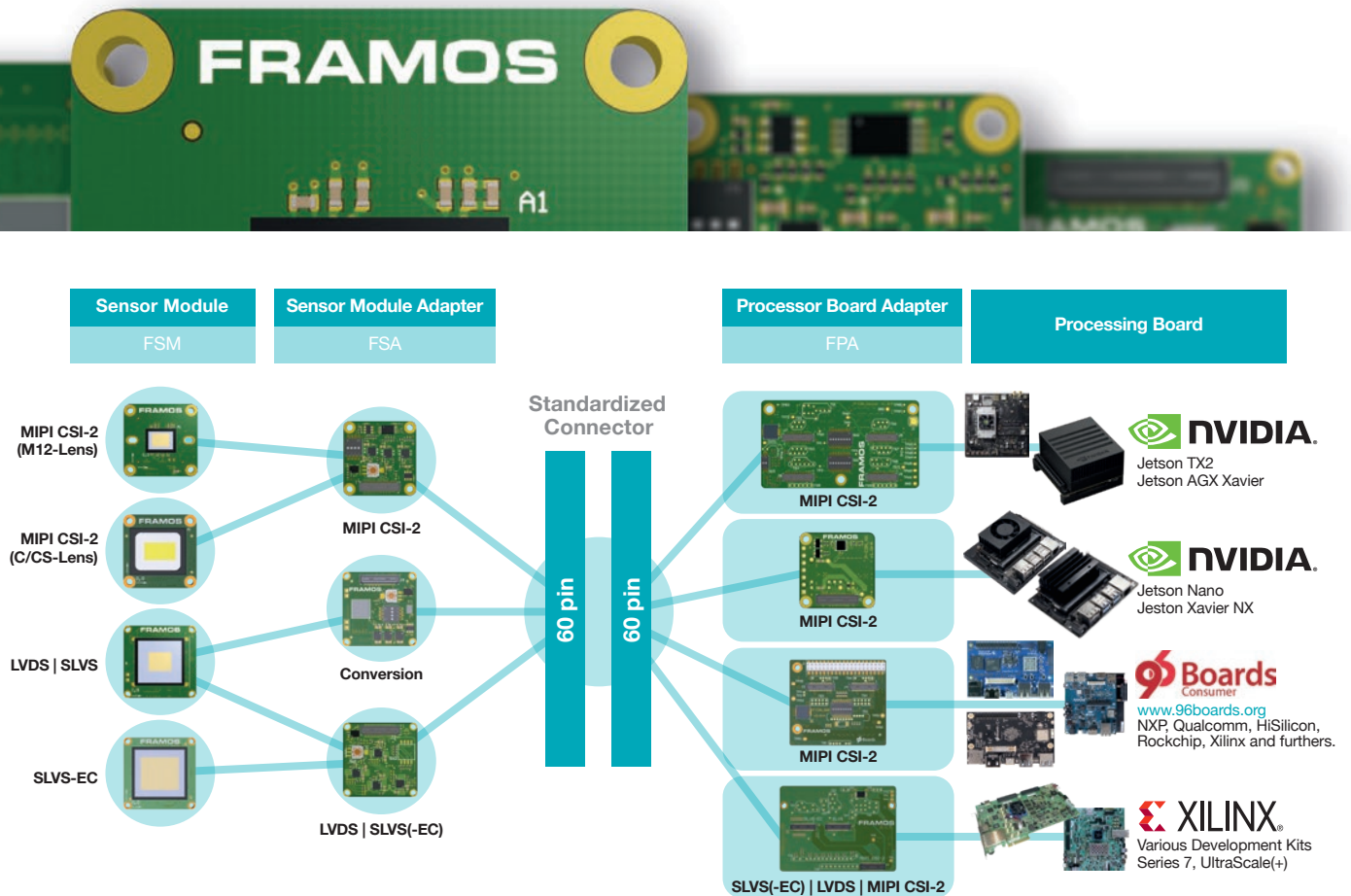
## THIS SYSTEM IS MORE THAN SIMPLY AN OFF-THE-SHELF PRODUCT

- Large lineup of Sony, ON Semiconductor, and Pyxalis image sensors on modules based on one connection standard
- Sophisticated adapter concept allows connections to every sensors and processors in the Ecosystem
- Ready-to-use demo drivers for selected lead platforms providing images immediately
- Electrical and software sources allow the design by well-known and reliable references
- Standard or customized sensor boards and assemblies, which simplify your production
- Strong partner network supporting your implementation from brick to complete solution - it's up to you to decide, which support you need.

## PROMPTLY GET TO A WORKING PROOF OF CONCEPT

FRAMOS provides you with the glue and logic needed to put your vision-based building blocks together and accompanies your entire development process from concept to mass production and beyond.

# EMBEDDED IMAGE SENSOR ECOSYSTEM



## FRAMOS SENSOR MODULES (FSM)

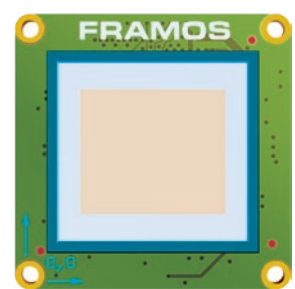
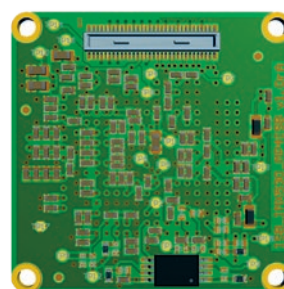
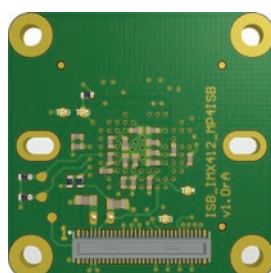
Electrical engineers design hardware, but most of them underestimate the difficulties of integrating image sensors as they require specific digital and analog design know-how. The speed of their design process depends on their experience with imagers especially in the analog domain, as it defines the robustness and grade of the image quality. Respins of new sensor designs, which means producing hardware each time, are unpredictable in that process. Each loop takes a couple of weeks or months to localize the source of an unwanted behavior, redesign hardware, produce it, and repeat testing.

FRAMOS provides sensors on modules with standardized connectors and mechanical design. The design is based on almost 20 years of camera design experience. Each module has been fully

tested and is verified by several customers. It contains all components required at the imager. Power generation, clock generation, and trigger logic are separated. This allows the customer to decide where this part is positioned within the system, and to protect the sensor from heat and disturbances.

## KEY BENEFITS

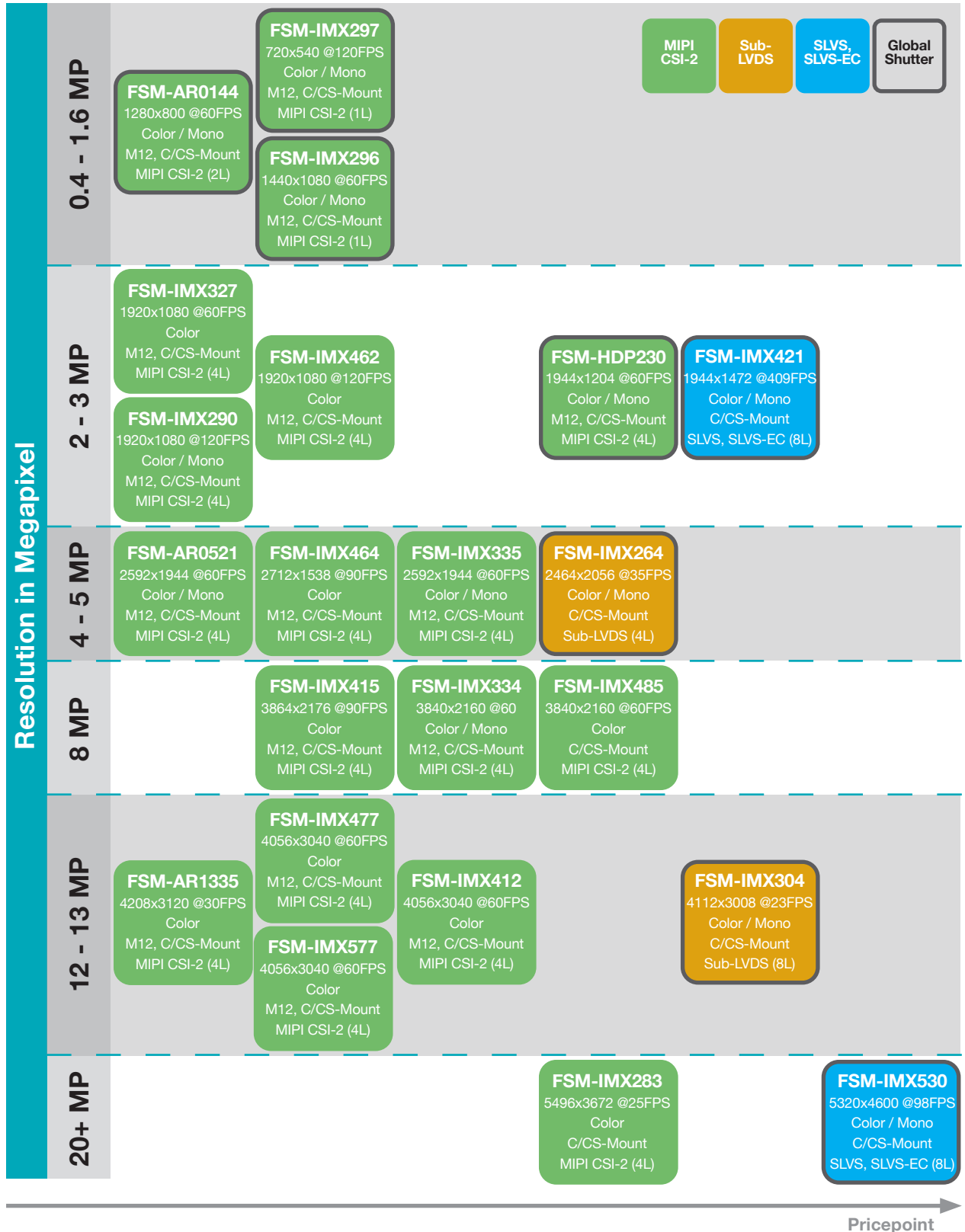
- **Image sensors on PCB** with connector, off-the-shelf
- **Large lineup** with resolutions from 0.4 to 24 MP rolling shutter and global shutter imagers
- **All sensors available with MIPI CSI-2** (D-PHY) output
- **Converter boards for SubLVDS and SLVS** imagers
- Standardized mechanics featuring **small footprints** of 26.5 x 26.5 mm<sup>2</sup> and 28 x 28 mm<sup>2</sup>



# EMBEDDED IMAGE SENSOR ECOSYSTEM



## FRAMOS SENSOR MODULE LINEUP



# EMBEDDED IMAGE SENSOR ECOSYSTEM



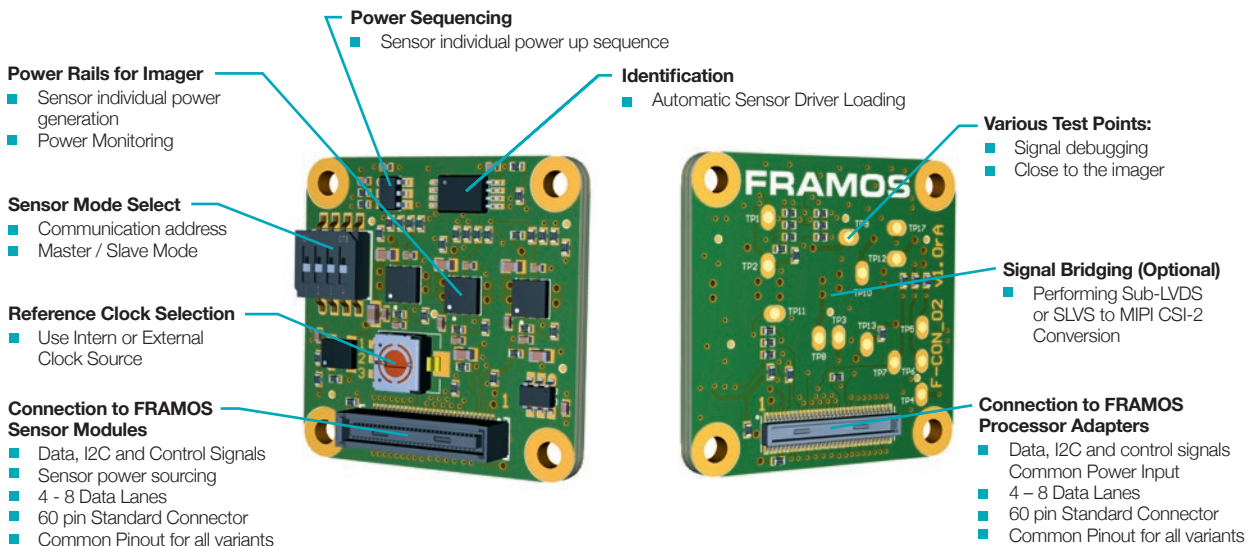
## ADAPTERS IN THE FRAMOS ECOSYSTEM

It is key for a project's success to start verification of the individual software application as early as possible using actual hardware. In any vision system, this hardware is primarily the image sensing part, combined with a processing part which is able to run the customer's implementation. However, different applications have different requirements for these components and need easy scoping and quick adjustment of the components where needed.

Hardware re-designs consume a lot of resources and time and can put the whole project at risk. Thus, one significant part of the FRAMOS Embedded Ecosystem is the accessibility of adapters for a large variety of sensor-processor combinations. They act as a glue and are at one time off-the-shelf connectivity and proven reference design for later phases. Therefore, main function groups are split into two separate boards:

- FRAMOS Sensor Adapters (FSA)
- FRAMOS Processor Adapters (FPA)

## FRAMOS SENSOR ADAPTERS (FSA)



## SENSOR RELATED FUNCTIONS BY FSA

FRAMOS Sensor Adapters (FSA) feature individual voltages, reference clocks, and processing functions like data conversion and signal processing. Therefore, they can be implemented by the customer at any point in the final system; individually, close to the sensor, on the processor board, or mixed in different ways.

- Contain periphery required to operate imagers
- Stacked piggyback to FRAMOS Sensor Module (FSM)
- Multiple sensors share the same FSA type
- Options with SubLVDS and SLVS to MIPI CSI-2 (D-PHY) conversion

## KEY BENEFITS

- **Better results** - less impact on image quality by neighboring components
- **Less redundancy** - customer can place the module on their mainboard and use existing resources; proofed electrical references are provided
- **Flexibility** - customer can add further functionality without touching the sensor implementation
- **“All-on-one-board customization”** - is possible as soon as the design concept has been fixed by the customer



# EMBEDDED IMAGE SENSOR ECOSYSTEM



## PROCESSOR BOARD CONNECTIVITY BY FPA

FRAMOS Processor Adapters (FPA) cover individual connectors, signals, voltages, and communication channels to 3rd-party processor boards. This adapter (an individual part) is the reference to implement the image sensing front-end into the customer central processing system. The FPA boards make it plug and play to use the sensor module portfolio to work with a large number of processor boards, providing ecosystem standard interface on various processor boards.

## SUPPORTED PROCESSOR DEVELOPMENT KITS

FRAMOS Processor Adapters with MIPI CSI-2 routing are provided off-the-shelf for the NVIDIA® Jetson™ TX2, Jetson AGX Xavier™, Jetson Nano™, and Jetson Xavier™ NX developer kits, as well as for the 96Boards Consumer Edition ecosystem with its over 16 single board computers. Further, a Xilinx specific FMC adapter establishes SubLVDS, SLVS, SLVS-EC and MIPI CSI-2 connectivity to a number of Xilinx development boards.

## FRAMOS PROCESSOR ADAPTERS (FPA)

### Up to four Individual Sensor Ports

All signals are available on each port

- Generic Power Supply
- 4 - 8 Data Lanes
- Reference Clock
- Synchronization Signals
- Communication (I2C / SPI)
- Further GPIOs

### Configurable EEPROM

- Hardware Identifier
- Enable / Disable / Protect

### Synchronize sensors

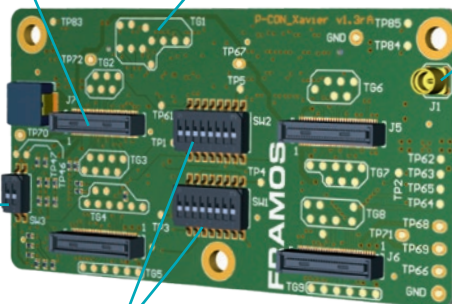
- Interconnect sync signals of multiple imagers
- Includes: XVS, XHS, MCLK

### Test Points for All Relevant Signals

- Clocks, Synchronization Signals
- Communication
- Further GPIOs

### External Trigger Input Line

- Optionally equipped



### Processor Board Specific Connector

- FPA Lineup for Multiple Platforms
- Routing all relevant signals
- Adjusted for the capabilities of each board

### Master Clock Signaling

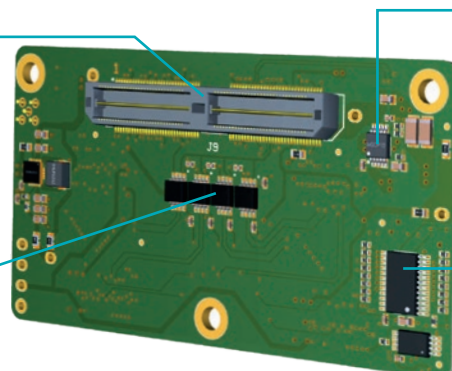
- Drive multiple sensors with one or two clock sources
- Ensure highest signal integrity

### Main Power Rails

- Sourcing FSAs with processor board independent voltages

### I2C Multiplexing

- Individual addressing of multiple sensors via one I2C bus



# EMBEDDED IMAGE SENSOR ECOSYSTEM



## SOFTWARE SUPPORT

With the hardware in place, there is still one significant, but often underestimated element missing, until one sees the first image on screen, or can even use it with his algorithms; the device driver. Working with proprietary hardware interfaces on both sensor and processor platform side requires solid know-how and time to understand and use the individual underlying architecture. Establishing communication and basic data streams is only the first difficult step, adding up with the complex sensor initialization sequences, and configuration scripts for every single feature. As a result, tuning the sensor settings for an immaculate image can become an issue and require a lot of time.

## ECOSYSTEM SOFTWARE PACKAGE

To support a smooth start and unburden developers from starting from scratch, the

Software Pack, along with the ecosystem, provides reference drivers for all sensors on selected processor platforms. The focus is to enable a quick start demonstrating the implementation of the fundamental functions that are required to utilize the sensor and receive one or multiple image streams. The application specific finalization of the driver can then happen seamlessly on project base by the customer or FRAMOS, extending or porting the Software Pack utilizing the provided driver sources.

## PLATFORM SUPPORT

Lead platform for all driver activities are the NVIDIA® Jetson™ TX2, Jetson AGX Xavier™, Jetson Nano™, and Jetson Xavier™ NX developer kits. Drivers for further platforms already exist and are available on request or can be designed on project basis.

```
Terminal
nvidia@nvidia-desktop:~$ sudo l2cdump -y 30 0x54
[sudo] password for nvidia:
No size specified (using byte-data access)
0 1 2 3 4 5 6 7 8 9 a b c d e f 0123456789abcdef
00: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....?framos-adap
10: 74 65 72 00 ff ff ff ff ff ff ff ff ff ff ff ff .....ter.....
20: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
30: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
40: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
50: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
60: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
70: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
80: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
90: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
a0: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
b0: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
c0: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
d0: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
e0: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
f0: ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff ff .....
nvidia@nvidia-desktop:~$
```

```
Terminal
13.903859] Extcon AUX2(HDMI) disabl
13.937997] tegra_nvdisp_handle_pd_e
13.937996] tegra_nvdisp_handle_pd_e
13.940125] tegradc 15200000.nvdisp
13.942559] tegra_nvdisp_handle_pd_e
13.943022] tegra_nvdisp_handle_pd_e
13.950944] Parent Clock set for DC
13.957663] tegradc 15200000.nvdisp
prod_c_hdmi_11in_223m
13.959239] tegradc 15200000.nvdisp
13.959255] tegradc 15200000.nvdisp
13.994376] extcon-disp-state exterr
13.994380] Extcon AUX2(HDMI) enabl
13.994577] tegradc 15200000.nvdisp
13.994558] tegradc 15210000.nvdisp
13.994568] tegradc 15220000.nvdisp
16.653565] fuse init (API version 7
16.757552] Bluetooth: BNEP (Ethernet
16.757560] Bluetooth: BNEP socket
17.565947] tegradc 15200000.nvdisplay: unblank
17.565964] tegradc 15210000.nvdisplay: blank - powerdown
17.565970] tegradc 15220000.nvdisplay: blank - powerdown
```

Image Sensor Module	Driver Name	EEPROM ID	FFA-4A/TXA I2C BUS ID
[MUX-CH] - 35, 36, 37, 38			
[FSH-AR0144]	ar0144	framos-ar0144-[MUX-CH]	30 [0], 32 [2], 34 [4], 36 [6]
[FSH-AR0144]	ar0144	framos-ar0144-[MUX-CH]	30 [0], 32 [2], 34 [4], 36 [6]

a) EEPROM on the FSN

Prerequisites:

- 1st pin of the DIP switch on FSA board set to OFF position.

**\*\*Note\*\*:** Some FSA modules do not have the DIP switch to enable/disable the on board EEPROM. There, on board EEPROM is enabled by default and care must be taken that only one EEPROM is written with Module ID and the other one is erased, or the same Module ID is written to FSA EEPROM and FSN EEPROM.

Linux for Tegra environment streaming

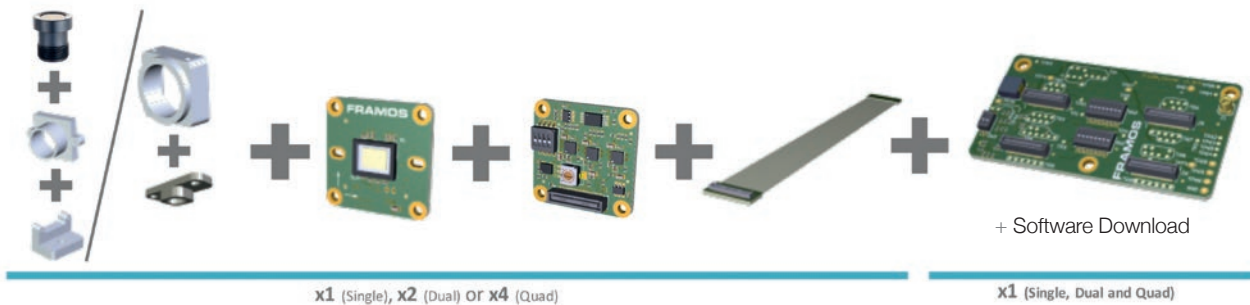
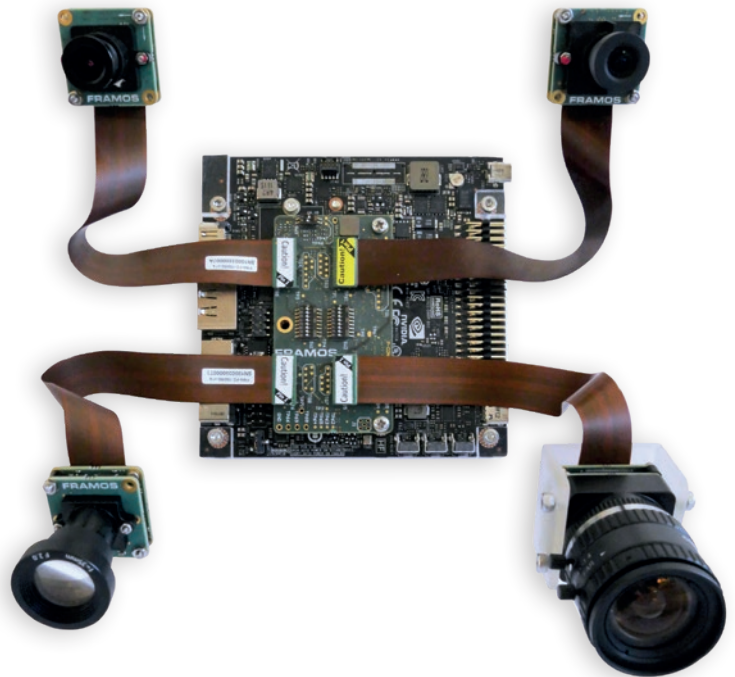
# EMBEDDED IMAGE SENSOR ECOSYSTEM



## DEVELOPMENT KIT

The FRAMOS Module Ecosystem consists of a large number of modules, adapters, and accessories. To start right away, we provide several development kits which include everything that is needed for a smooth startup. On request, this basic list can be individually extended by additional components:

- One or multiple FRAMOS Sensor Module(s) with M12 or C/CS lens with mount
- FRAMOS Sensor Adapter for the selected sensor module(s)
- FRAMOS Processor Adapter for up to four modules on a specified platform
- Accessories per sensor module (150 mm Flex Cable, 1/4" Tripod Adapter)
- Download of binary Software Pack



## SOFTWARE PACK CONTENTS

- Device drivers for the MIPI CSI-2 interface (processor board) supporting image streaming and fundamental sensor features
- Sub-device drivers for the image sensor, featuring communication, initialization, streaming, and basic configuration in master mode, multiple presets for resolution and pixel format as well as access to integration time, gain, and sensor mode
- Basic image processing example for color imagers (software and Libargus)
- Source code and built environment



# EMBEDDED IMAGE SENSOR ECOSYSTEM

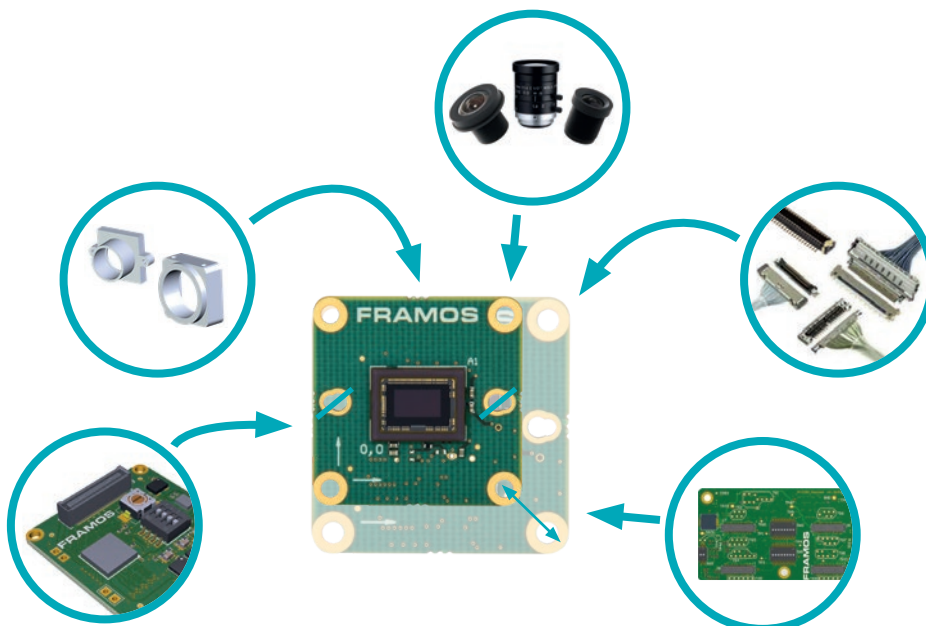


## FRAMOS' EMBEDDED VISION SERVICES

- **Unbiased Consulting** - provided by industry experts who listen and understand your requirements and find the right solutions.
- **Analysis of Requirements** - to choose right vision components and provide guidance in selecting the best sensor, optics and interface.
- **Product Selection Support** - for navigating our diverse supplier network to match their products with your requirements.
- **Measurements & Testing** - services that evaluate and compare image sensors
- **Evaluation Boards & RDK's** - assist in understanding how to design sensors into your vision system.
- **Rapid Prototyping** - for assessing and comparing various options and solutions that with parallelizing development.
- **Experienced Design-in Support** - assists during development by answering questions and providing the necessary data, test measurements and development tools needed to create a vision system.
- **Custom Designs** - Our engineering team can be your trusted development partner for an individual approach to sensor modules, IP and SoC solutions. We support you in customizing existing solutions or completely developing new solutions tailor-made for your embedded vision system.

With its practical industry and project experience, Framos serves their clients as a technical consultant, development partner, and external supplier enabling our customers to develop cutting-edge imaging solutions while shortening their times to market.

## STANDARD OFF-THE-SHELF CUSTOMIZED OR FULL CUSTOM DESIGN!



## CONTACT

If you have additional questions about this technology or how it would benefit you, our FRAMOS imaging experts are available to answer any questions. We can be reached at: [info@framos.com](mailto:info@framos.com)