

FSM-IMX421 Datasheet

Sony IMX421LLJ / IMX421LQJ Sensor Module

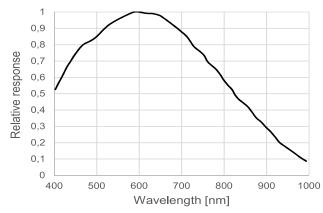
FRAMOS Sensor Module



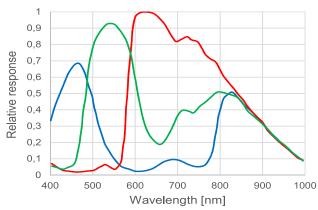
Key Benefits & Features:

- 2.8 Mpx Sony CMOS Global Shutter sensor module, ready to embed!
- All FSMs are part of a rapid prototyping ecosystem, consisting of:
 - ✓ Adapters to various processing boards
 - Design sources for deep embedding
 - Various accessories and design in services

FSM-IMX421M (Monochrome):



FSM-IMX421C (Color):

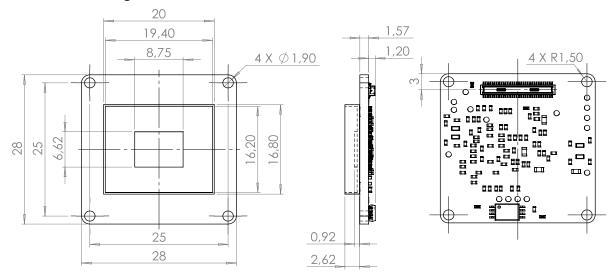


Specification							
Model Name	FSIV	I-IMX421M / FSM-IMX421C (V1A)					
Image Sensor							
Vendor / Name	Son IMX	y 421LLJ / IMX421LQJ					
Shutter Type	CM	OS Global Shutter					
Chromaticity	Colo	or / Mono					
Optical Format	2/3	и					
Pixel Size	4.5	x 4.5 μm					
Max. Resolution	2.8	Mpx / 1944 x 1472 px					
Framerate (max.)	409	FPS					
Bit Depth(s)	12 k	bit					
Interface							
Data Interface		SLVS-EC (1 / 2 / 4 / 8 Lane)					
Communication Interfa	ce	I ² C (4-wire serial)					
Drive Frequency(s)		37.125 / 54 / 74.25 MHz					
Input Voltages		1.2V, 1.8V, 3.3V					
Interface Connector		Hirose DF40C-60DP-0.4V(51)					
EEPROM (Sensor ID)		Yes					
Mechanical							
Dimensions (HxWxD)		28 mm x 28 mm					
Environmental							
Operating Temperature	_	-30°C to +75°C (function)					
operating remperature	-	-10°C to +60°C (performance)					
Storage Temperature		-40°C to +85°C					
Ambient Humidity		20% to 95% RH, non condensing					
Software Support							
Driver		Not provided.					
Supported Platform(s)		SLVS-EC Reference Implementation for Xilinx FPGAs					
Software Version(s)		Vivado 2017.4 (or above)					
Suggested Accessories	& Ad	apters					
Compatible FSA							
Recommended Devkit(s)	FSM-IMX421x/XX1_Devkit (Xilinx)					
Lens Mounts		C/CS-Mount option					
A matrix with compatible	Senso	r Adapters (FSA) and Processor Board Adapters					

A matrix with compatible Sensor Adapters (FSA) and Processor Board Adapter (FPA) for various setups can be found at the end of this document.

Mechanical Drawing

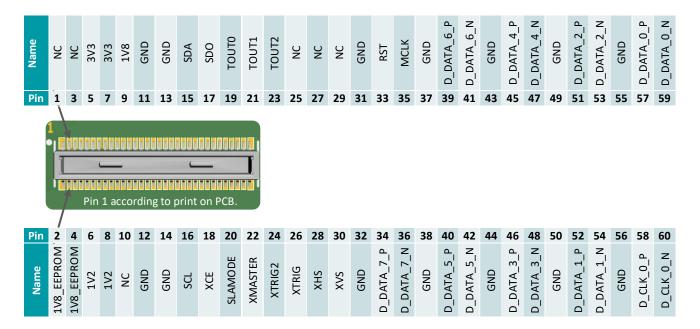
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Sensor image optical center is in mechanical board center.

Connector Pinout

Type: Hirose DF40C-60DP-0.4V(51) Mating Type: Hirose DF40HC(4.0)-60DS-0.4V(51)



Signals are routed directly from image sensor to connector. Details on specific signals are described in the respective image sensor datasheet.

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1 FRAMOS Sensor Module Ecosystem

The FSM Ecosystem consists of FRAMOS Sensor Modules, Adapters, Software and Sources, and provides one coherent solution supporting the whole process of integrating image sensors into embedded vision products.

During the evaluation and proof-of-concept phase, off-the-shelf sensor modules with a versatile adapter framework allow the connection of latest image sensor technology to open processing platforms, like the NVIDIA Jetson Family or the 96boards.org standard. Reference drivers and sample applications deliver images immediately after installation, supporting V4L2 and an optional derivate API providing comfortable integration. Within the development phase, electrical design references and driver sources guide with a solid and proven baseline to quickly port into individual system designs and extend scope, while decreasing risk and efforts.

To simplify and relieve the whole supply chain, all FRAMOS Sensor Modules and adapters are optimized and ready for delivery in volume and customization with pre-configured lens holder, lens and further accessories.

Off-the-Shelf Hardware

- FRAMOS Sensor Modules (FSM) from stock, ready for evaluation and optimized for initial mass production.
- Versatile adapter framework, allowing flexible testing of different modules, on different processing boards:
 - FRAMOS Sensor Adapter (FSA): Everything the specific sensor needs for operation
 - FRAMOS Processor Adapter (FPA): Connect up to four FSM + FSA to a specific processor board
- From lenses, mechanics and cables, all needed imaging accessories from one hand

Ready to go Software Package

- Drivers with basic sensor integration:
 - Platform specific device drivers
 - V4L2 subdevice drivers for specific image sensors (low-level C API)
- Streamlined V4L2 library (LibSV) with comfortable and generic C/C++ API
- Example applications demonstrating initialization, main configuration and image stream processing

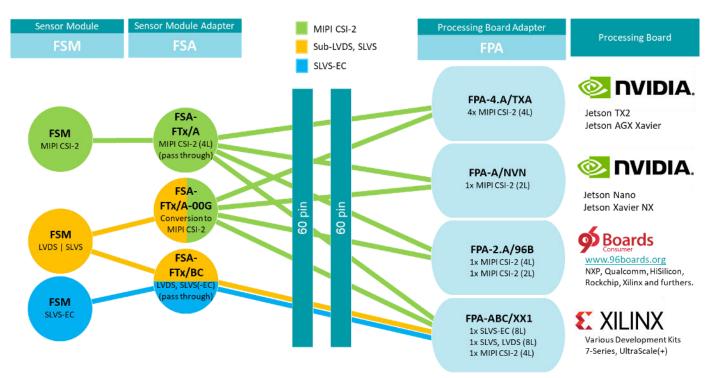
Further to the off-the-shelf hard- and software, the Ecosystem supports you on project basis with:

- Driver sources allowing the focus on application specific scope and sensor features
- Electrical references for FSA and FPA, supporting quick and optimized embedding of FSMs
- Engineering services via FRAMOS and its partners, allowing you to focus on your product's unique value

1.1 Ecosystem Overview

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The figure below shows a map of compatibility with all components inside the Ecosystem. Every element (or hardware) and connection displayed in **Green** operates with native MIPI CSI-2 (D-PHY) data.



Every component and connection displayed in Orange or Blue operates with proprietary (Sub-LVDS, SLVS) or standardized (SLVS-EC) LVDS data, that requires further attention to the physical processing of the image data by either data conversion or specific FPGA IP. Users of MIPI CSI-2 based processing systems are supported by FSM specific data conversion located on dedicated FRAMOS Sensor Adapters (FSAs).

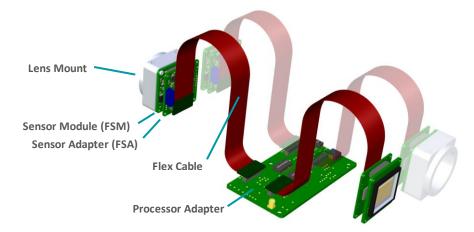


Figure 1: Assembly of a typical Sensor Module Development Kit

Specification and compatibility of all individual components are listed in the appropriate chapter of the full datasheet. Access to software and drivers is only granted with the purchase of the appropriate development kit. Electrical design sources, support and services are provided on individual basis, they are not part of the development kit or component purchase.

1.2 Materials and Services

Below you can find a list of materials and services as part of the FRAMOS Sensor Module Ecosystem.

Hardware

- FRAMOS Sensor Module Development Kits
- Individual Parts:
 - FRAMOS Sensor Modules
 - FRAMOS Sensor Adapters
 - FRAMOS Processor Adapters
 - FRAMOS Module Accessories (Cables, Mounts)

Software (part of the Development Kit)

- Software Package for NVIDIA Jetson AGX Xavier, Nano, TX2 and Xavier NX
- Software Package for DragonBoard 410c (96Boards)
- Xilinx FPGA reference implementation for SLVS-EC (Sony IMX421, IMX530)

Design Sources (on Project Basis)

- Software Driver Sources
- Electrical References for FSA, FPA (Schematics)

Design Services

- Off-the-shelf hardware customization including size, shape, connector and extended functionality
- Software customization and extension
 - Additional processor board support
 - Further sensor features and image (pre-)processing
- Integration of additional sensors
- Optimization for volume production
- Lens assembly and alignment
- System / solution development
- Production and integration

2 Software Package and Drivers

As FRAMOS we know that the getting started with a new technology is the biggest challenge. The idea behind the Software Package is to enable embedded software engineers to get quickly to a streaming system and provide at the same time all tools that are needed to extend and adapt it according the individual needs of the application.

What the software package and driver are:

- A reference for a custom sensor implementation
- Demonstrating how to use the required interfaces
- Demonstrating how to communicate with the image sensor
- Demonstrating how to generaly initialize and configure the image sensor
- Provide initial image streaming output to the user space
- Demonstrating how to run basic image processing on pixel data

Supported Processor Platforms

The table below shows which platforms are supported by the standard driver package, and how many FSMs can at maximum be operated in parallel.

Sensor Module	NVIDIA Jetson TX2	NVIDIA AGX Xavier	NVIDIA Jetson Nano, Xavier NX	DragonBoard 410c	96Boards Consumer Edition	Xilinx Development Boards		
FSM-AR0144	2	1	2					
FSM-AR0521	4	1	2	2				
FSM-AR1335	2	1	2					
FSM-HDP230	4	4	2					
FSM-IMX264	2	4	-					
FSM-IMX283	2	4	-					
FSM-IMX290	4	1	2	2				
FSM-IMX296	4	1	2	2				
FSM-IMX297	4	1	2			ver development ject basis.		
FSM-IMX304	2	4	-		HW only driv			
FSM-IMX327	2	1	2	2				
FSM-IMX334	2	4	-		onproj			
FSM-IMX335	L	1	2					
FSM-IMX412	4	1	2	2				
FSM-IMX415	2	1	2					
FSM-IMX462	L	1	2					
FSM-IMX464	L	1	2					
FSM-IMX477	L	1	2					
FSM-IMX485	2	1	2					
FSM-IMX565	4	1	2					
FSM-IMX577	4	1	2					
FSM-IMX530	2	4	-			11		

Table 1: Ecosystem Software Package - Supported number of FSMs per processing board

¹ SLVS-EC based FPGA reference implementation as part of the SLVS-EC RX IP Core offering.

2.1 Reference Software: NVIDIA Jetson Family

The software package provided with the Development Kits of the FRAMOS Sensor Module Ecosystem provided for NVIDIA Jetson platforms provides a reference implementation of sensor and device drivers for MIPI CSI-2. It contains a minimum feature set demonstrating how to utilize the platform specific data interface and communication implementation, as well as the initialization of the image sensor and implementation of basic features.

Package Content:

- Platform and device drivers with Linux for Tegra Support
- V4L2 based subdevice drivers (low-level C API)
- Streamlined V4L2 library (LibSV) providing generic C/C++ API
- Image Pre-Processing Examples:
 - OpenCV (Software)
 - LibArgus (Hardware)

Supported Devices:

- NVIDIA Jetson Nano Developer Kit (B01)
- NVIDIA Jetson TX2 Developer Kit
- NVIDIA Jetson Xavier NX Developer Kit
- NVIDIA Jetson AGX Xavier Developer Kit

2.1.1 Platform and Sensor Device Drivers

The driver divides into two main parts that are configured in separate ways – the Image Modes and the General Features of the image sensor.

Image Modes

These are major attributes that have impact to the image data stream formatting. They require a static preconfiguration within the device tree (DT):

- Image / streaming resolution
- Pixel format / bit depth
- Data rate / lane configuration

Each driver provides access to 3 - 5 pre-built configurations, reflecting the main operation modes of the imager. Beside the full resolution, that is always available, they allow to receive image streams in common video resolutions like VGA, Full HD and UHD as they are supported or make sense by the imagers, and utilize sensor features like ROI and binning.

They act as an example for implementation and usage and are available as source. Due to the size limitation of the device tree, it is not possible to integrate an extensive set of options.

General Features

These are attributes of the image sensor that do not manipulate the data stream formatting. The drivers provided with the Software Pack integrate the sensor features as shown in the table below.

Pre-Implemented Features per Model	Gain (Analog / Digital)	Frame Rate	Exposure Time	Flip / Mirror	IS Mode (Master / Slave)	Sensor Mode ID	Test Pattern Output	Black Level	HDR Output	Broadcast	Data Rate	Synchronizing Master
FSM-AR0144												
FSM-AR0521												
FSM-AR1335												
FSM-HDP230												
FSM-IMX264												
FSM-IMX283												
FSM-IMX290												
FSM-IMX296		*										
FSM-IMX297		*										
FSM-IMX304												
FSM-IMX327												
FSM-IMX334												
FSM-IMX335												
FSM-IMX412												
FSM-IMX415												
FSM-IMX462												
FSM-IMX464												
FSM-IMX477												
FSM-IMX485												
FSM-IMX530												
FSM-IMX565												
FSM-IMX577												

Table 2: Supported sensor features on NVIDIA Jetson TX2 / AGX Xavier

*Only supported in all pixel mode

Further features, as been supported by the image sensor, can be integrated into the driver sources using the image sensor datasheet.

2.1.2 Image Pre-Processing Examples

The provided image processing examples show the general mechanisms of data handling, for an image processing using 3rd-party IP. Both, the OpenCV and the LibArgus examples do not output data that is tuned for best visual experience.

LibArgus Example:

- Closed source ISP implementation
- Using hard ISP in NVIDIA Jetson SOCs
- Most performant option
- Example Implementation: Full but not tuned image pipeline, displaying

Color tuning and lens correction needs to be calibrated for every image sensor separately and depends on sensor and lens attributes as well as illumination situation.

Image Pre-Processing Features per Model	Bad Pixel Correction	Noise Reduction	Black Level Comp.	Auto Exposure, Gain	Auto White Balance	Demosaic	Color Correction	Color Artifact Suppr.	Downscaling	Edge Enhancement
FSM-AR0144										
FSM-AR0521										
FSM-AR1335										
FSM-HDP230										
FSM-IMX264										
FSM-IMX283										
FSM-IMX290										
FSM-IMX296										
FSM-IMX297										
FSM-IMX304										
FSM-IMX327										
FSM-IMX334										
FSM-IMX335										
FSM-IMX412										
FSM-IMX415										
FSM-IMX462										
FSM-IMX464										
FSM-IMX477										
FSM-IMX485										
FSM-IMX530										
FSM-IMX565										
FSM-IMX577										

Default Config

Image streaming is performed through the LibArgus pipeline, using a common configuration. It demonstrates the usage of LibArgus but is not optimized for the certain sensor configuration and might not lead to good image representation.

Note: Systems are diverse, as the utilized lenses, filters and illuminations are. As NVIDIA camera partner, FRAMOS provides individual ISP calibrations services for the customer hardware on per project basis.

OpenCV Example:

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- Open software library
- Easy to use and large feature set
- Extremely performance hungry (CPU)
- Not recommended for pre-processing
- Example Implementation: Demosaicing, Displaying

	Image Pre-Processing Features per Model	Bad Pixel Correction	Noise Reduction	Black Level Comp.	Auto Exposure, Gain	Auto White Balance	Demosaic	Color Correction	Color Artifact Suppr.	Downscaling	Edge Enhancement
FS	M-AR0144										
FS	M-AR0521										
FS	M-AR1335										
-	M-HDP230										
FS	M-IMX264										
FS	M-IMX283										
FS	M-IMX290										
FS	M-IMX296										
FS	M-IMX297										
FS	M-IMX304										
FS	M-IMX327										
FS	M-IMX334										
FS	M-IMX335										
FS	M-IMX412										
FS	M-IMX415										
FS	M-IMX462										
FS	M-IMX464										
FS	M-IMX477										
FS	M-IMX485										
FS	M-IMX530										
FS	M-IMX565										
FS	M-IMX577										

Table 4: Implemented features in OpenCV example

Due to limited performance and extreme resource utilization, it is not planned to enhance the image processing support on software side.

3 Ecosystem Compatibility Matrix

3.1 Hardware Support

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The following matrix shows the compatibility of FSMs, FSAs and FPAs to each other. The FSAs differentiate to each other by supplied voltages, power up sequence, generated clock (oscillator) and physical attributes.

			51-2 (D-PHT)	o a tp a t					
		FSM-IMX290							
		FSM-IMX327							
		FSM-IMX334							
		FSM-IMX335							
		FSM-IMX462							
		FSM-IMX464							
Item		FSM-IMX485	FSM-IMX297	FSM-AR1335	FSM-IMX415	FSM-IMX283	FSM-AR0144	FSM-HDP230	FSM-IMX565
FSA-FT1/A	FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²								
FSA-FT3/A		FPA-4.A/TXA FPA-A/NVN ³ FPA-2.A/96B FPA-ABC/XX1 ²							
FSA-FT6/A			FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²						
FSA-FT7/A				FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²					
FSA-FT11/A					FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²				
FSA-FT12/A						FPA-4.A/TXA FPA-2.A/96B FPA-ABC/XX1 ²			
FSA-FT13/A							FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²		
FSA-FT19/A								FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²	
FSA-FT26/A									FPA-4.A/TXA FPA-A/NVN FPA-2.A/96B FPA-ABC/XX1 ²

Sensor Modules with MIPI CSI-2 (D-PHY) Output

Table 5: Ecosystem Compatibility Matrix – Native CSI-2 (D-PHY) FSMs

² Not verified, Xilinx Development Board with hard MIPI CSI-2 / D-PHY interface.

³ FSM-IMX334 is not supported due to the sensor requiring 4-lanes MIPI.

Sensor Modules with (Sub-) LVDS and SLVS Output

Item	Data Output (FSA)	FSM-IMX264	FSM-IMX304	FSM-IMX421	FSM-IMX530
FSA-FT14/A-00G	MIPI CSI-2	FPA-4.A/TXA FPA-2.A/96B FPA-ABC/XX1 ²			
FSA-FT15/A-00G	MIPI CSI-2		FPA-4.A/TXA FPA-2.A/96B FPA-ABC/XX1 ²		
FSA-FT18/A-00G	MIPI CSI-2				FPA-4.A/TXA FPA-2.A/96B FPA-ABC/XX1 ²
FSA-FT18/BC	SLVS, SLVS-EC				FPA-ABC/XX1
FSA-FT20/BC	SLVS-EC			FPA-ABC/XX1	

Table 6: Ecosystem Compatibility Matrix – Sub-LVDS, SLVS and SLVS-EC FSMs