SONY

Ver.1.1

IMX267LLR

Diagonal 16.1 mm (Type 1) CMOS solid-state Image Sensor with Square Pixel for Monochrome Cameras

Description

The IMX267LLR is a diagonal 16.1 mm (Type 1) CMOS active pixel type solid-state image sensor with a square pixel array and 8.95 M effective pixels. This chip features a global shutter with variable charge-integration time. This chip operates with analog 3.3 V, digital 1.2 V, and interface 1.8 V triple power supply, and has low power consumption. High sensitivity, low dark current and low PLS characteristics are achieved.
(Applications: FA cameras, ITS cameras)

Features

- ◆ CMOS active pixel type dots
- ◆ Built-in timing adjustment circuit, H/V driver and serial communication circuit
- ◆ Global shutter function
- ◆ Input frequency
  37.125 MHz / 74.25 MHz / 54 MHz
- ◆ Number of recommended recording pixels: 4096 (H) × 2160 (V) approx. 8.85 M pixels

  Readout mode
  All-pixel scan mode
  Vertical / Horizontal 1 / 2 Subsampling mode
  ROI mode
  Vertical / Horizontal ‐ Normal / Inverted readout mode
- ◆ Readout rate
  Maximum frame rate in

  All-pixel scan mode: 12 bit: 32.2 frame/s
- ◆ 12-bit A/D converter
- ◆ CDS / PGA function
  0 dB to 24 dB: Analog Gain (0.1 dB step)
  24.1 dB to 48 dB: Analog Gain: 24 dB + Digital Gain: 0.1 dB to 24 dB (0.1 dB step)
- ◆ I/O interface
  Low voltage LVDS (150 mVp-p) serial (4 ch / 8 ch switching) DDR output
- ◆ Recommended lens F number: 2.8 or more (Close side)
- ◆ Recommended exit pupil distance: –100 mm to –∞

Pregius

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Device Structure

◆ CMOS image sensor			
◆ Image size	Diagonal 16.1 mm (Type 1)	Approx. 8.95 M pixels	All-pixel
◆ Total number of pixels	4112 (H) × 2186 (V)	Approx. 8.99 M pixels	
◆ Number of effective pixels	4112 (H) × 2176 (V)	Approx. 8.95 M pixels	
◆ Number of active pixels	4112 (H) × 2176 (V)	Approx. 8.95 M pixels	
◆ Number of recommended recording pixels	4096 (H) × 2160 (V)	Approx. 8.85 M pixels	All-pixel
◆ Unit cell size	3.45 µm (H) × 3.45 µm (V)		
◆ Optical black	Horizontal (H) direction: Front 0 pixel, rear 0 pixel		
	Vertical (V) direction: Front 10 pixels, rear 0 pixel		
◆ Package	226 pin LGA		

Image Sensor Characteristics

(Tj = 60 °C)

Item		Value	Remarks
Sensitivity (F8)	Typ.	915 mV	1/30 s accumulation
Saturation signal	Min.	1001 mV	

Basic Drive Mode

Drive mode	Recommended number of recording pixels	Maximum frame rate [frame/s]	Output interface	ADC [bit]
All pixel	4096 (H) × 2160 (V) approx. 8.85 M pixels	32.2	Serial LVDS 8 ch	12
All pixel (Vertical / Horizontal 1/2 subsampling)	2048 (H) × 1080 (V) approx. 2.21 M pixels	63.5	Serial LVDS 8 ch	12

Image Sensors for Industrial Applications:
https://www.sony.net/cis-industry